

**Amendments to the Specification:**

Please replace paragraph [0041] as follows:

**[0041]** Fig. 6 illustrates a prior art diagram of a generic system and process in which serial processing involving Phases I and II, like ~~Figs~~ Fig. 1 and-, is illustrated between a UE and one of either a home network or a visited network network. Like the processing described above in conjunction with Figs. 1 and 5, the processing is serial involving Phases I and II with the attendant time delay. A single network entity in the home or visited network is the point of exchange with the Request and Answer messages involving Phases I and II.